



PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

**Complete if Known**

Application Number	10/790,492
Filing Date	March 1, 2004
First Named Inventor	Lingyi A. Zheng
Art Unit	2822
Examiner Name	T. M. Thomas
Attorney Docket Number	MIO 0082 N2/40509.292

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
JMJ		US- 5,023,683	06/11/1991	Yamada	
		US- 5,624,865	04/29/1997	Schuegraf et al.	
		US- 5,905,280	05/18/1999	Liu et al.	
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		US- 6,175,129	01/16/2001	Liu et al.	
		US- 6,207,494	03/27/2001	Graimann et al.	
		US- 2001/0024387 A1	09/27/2001	Raaijmakers et al.	
		US- 2002/0047151 A1	04/25/2002	Kim et al.	
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Error**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>4</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
JMJ		KR 2002002722	06/30/2000	Yoo		
JMJ		KR 2002050364	12/21/2000	Cho		

Examiner  
Signature

T. M. Thomas

Date  
Considered

06-18-05

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Sheet

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of

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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
X		0.13 um CMOS DEVICE CHARACTERISTICS, <a href="http://www.semiconductor.net/semiconductor/issues/issues/2000/200001/images/Six0001_July-31-2001_p.1">http://www.semiconductor.net/semiconductor/issues/issues/2000/200001/images/Six0001_July-31-2001_p.1</a>	
X		PETER GINGER, Atomic Layer Deposition Targets Thin Films, Semiconductor International September 1999, Technology News, p. 1,2	
X		PROFESSOR MARKKU LESKELA et al., Fabrication of Thin Films for Electronics by Atomic Layer Deposition and Electrodeposition, Communications URL: <a href="http://193.167.96.38/content/eng/cfm?fram=users/177/2048_cfm">http://193.167.96.38/content/eng/cfm?fram=users/177/2048_cfm</a> , p. 1	
X		GENUS, What is ALD? p. 1	
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Examiner Signature	T. M. Thomas	Date Considered	06-18-05
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